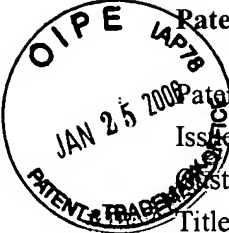


10/730641

Q0PC



Patent 6,953,983

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,953,983 B2

Docket No: 1303.041US2

Issue Date: October 11, 2005

Patentee: Paul A. Farrar

Customer No.: 21186

Confirmation No.: 2954

Title A SEMICONDUCTOR DEVICE COMPRISING LOW DIELECTRIC CONSTANT STI WITH SOI DEVICES

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: CERTIFICATE OF CORRECTION BRANCH

JAN 27 2006

of Correction

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

PAUL A. FARRAR

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6900

Date: 1-23-06

By: David C. Peterson
Reg. No: 47,857
DCP: rbs

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 23 day of January 2006

Ryan Saunders
Name

Signature

JAN 30 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar

Examiner: Fenty, Jesse

Patent No.: 6,953,983 *B2*

Group Art Unit: 2815

Issue Date: October 11, 2005

Docket No: 1303.041US2

Title: A SEMICONDUCTOR DEVICE COMPRISING LOW DIELECTRIC CONSTANT STI WITH SOI DEVICES

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN: CERTIFICATE OF CORRECTION BRANCH

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer No: 21186

By: 


Name: David C. Peterson

Reg. No.47,857

DCP:CMG:rbs

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 23 day of January 2006.

Ryan Saunders
Name


Signature

JAN 30 2006

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,953,983 *B2*

Page (1) of 1

DATED : October 11, 2005

INVENTOR(S) : Paul A. Farrar

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (54), in "Title", in column 1, line 1, Insert - - A
SEMICONDUCTOR DEVICE COMPRISING - - before "LOW".

On the face page, in field (56), under "Other Publications", in column 2, line 4, delete
"400-406." and insert - - 400-408. - -, therefor.

In column 6, line 58, delete "40 μ n/m° C." and insert - - 40 μ m/m° C. - -, therefor.

MAILING ADDRESS OF SENDER:

SCHWEGMAN, LUNDBERG, WOESSNER, & KLUTH, P.A.
P.O. BOX 2938
Minneapolis, MN 55402

Atty Docket No: 1303.041US2

PATENT NO. 6,953,983

No. of additional copies



JAN 31 2006

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,953,983 *B2*

Page (1) of 1

DATED : October 11, 2005

INVENTOR(S) : Paul A. Farrar

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (54), in "Title", in column 1, line 1, Insert - - A
SEMICONDUCTOR DEVICE COMPRISING - - before "LOW".

On the face page, in field (56), under "Other Publications", in column 2, line 4; delete
"400-406." and insert - - 400-408. - -, therefor.

In column 6, line 58, delete "40 μ n/m° C." and insert - - 40 μ m/m° C. - -, therefor.

MAILING ADDRESS OF SENDER:

SCHWEGMAN, LUNDBERG, WOESSNER, & KLUTH, P.A.
P.O. BOX 2938
Minneapolis, MN 55402

Atty Docket No: 1303.041US2

PATENT NO. 6,953,983

No. of additional copies



JAN 30 2006